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## U.S. UTILITY Patent Application

APPL NUM 10048192	FILING DATE 06/03/2002	CLASS 438	SUBCLASS E01	GAU 2812	EXAMINER TSC
<b>**APPLICANTS:</b> Engelhardt Manfred; Weinrich Volker;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/DE00/02555 07/21/2000					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> GERMANY 199 35 130.9 07/27/1999					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 12816-042001	
TITLE : Method for producing a semiconductor memory element					
U S DEPT. OF COMM./PAT & TM.-PTO-436L(Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
		Primary Examiner	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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